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By:

LINDA E. HASTINGS

Any fee due as a result of this paper, not covered by an enclosed check, may be charged on Deposit Acct. No. 50-1290.

Attorney Docket No.:100806-00246 (NECR 20.838)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor: Ichiro YAMAMOTO  
Naohiko KIMIZUKA

Serial No.: 10/749,915

Filed: December 31, 2003

Title: **SEMICONDUCTOR DEVICE INCLUDING P-CHANNEL  
TYPE TRANSISTOR, AND PRODUCTION METHOD FOR  
MANUFACTURING SUCH SEMICONDUCTOR DEVICE**

Examiner: Scott B. Geyer

Group Art Unit: 2829

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**ELECTION**

SIR:

In response to the Office Action mailed on January 19, 2005, the period for responding thereto having been set to expire after February 19, 2005, Applicant hereby elects Group II, claims 6-12, for prosecution at this time. Applicant reserves the right to

file a Divisional Application based on claims 1-5, which is being filed concurrently herewith. Early examination on the merits is earnestly solicited.

Any fee due with this paper, not fully covered by an enclosed check, may be charged on Deposit Account 50-1290.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Michael I. Markowitz", written over a horizontal line.

Michael I. Markowitz  
Reg. No. 30,659

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ATTORNEY DOCKET NO.: NEC 20.458

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